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|----------------------------|-----------------|--------------------|-----------------|
| SIZE/尺寸:                   | DESIGNED BY/设计: | <b>Hysine®</b>     |                 |
| SCALE/比例: 1:1              | DRAWN BY/绘图: 徐林 |                    |                 |
| CHECKED BY/校对:             | ENGR/工程师:       | TITLE/标题:          | DRAWING NO /图号: |
| APPROVED BY-DATE/<br>审核日期: | DATE/日期:        | <b>BR-MODBUS模块</b> | FILE NAME/文件名:  |